FREE LIVE INTERACTIVE WEBINARS on MATLAB & Simulink

ΦΕΒΡΟΥΑΡΙΟΣ 2009

Πέμπτη 19/02/2009
Tips & Tricks: Getting Started Using Optimization with MATLAB

Το σεμινάριο έχει διάρκεια 1 ώρα, και θα μεταδοθεί ζωντανά μέσω internet σε δύο ίδια Sessions.

- *Εγγραφή για τις 16.00*
- *Εγγραφή για τις 21.00*

Description:

Engineers and scientists across all major industries use optimization to find better solutions to their problems. In this webinar, we'll highlight optimization products offered by The MathWorks, including MATLAB, Optimization Toolbox, and Genetic Algorithm and Direct Search Toolbox. We'll summarize the capabilities of each product and discuss the benefits of running your optimizations from the MATLAB environment. Product demonstrations will show how you can use our tools to find solutions to real-world optimization problems, and new and experienced users will learn best practices for using MATLAB optimization products through a "tips and tricks" format.

Τρίτη 24/02/2009
Introduction to Stateflow for Signal Processing and Communications Applications

Το σεμινάριο έχει διάρκεια 1 ώρα, και θα μεταδοθεί ζωντανά μέσω internet σε δύο ίδια Sessions.

- *Εγγραφή για τις 16.00*
- *Εγγραφή για τις 21.00*

Description:

In this webinar we introduce Stateflow for modeling and simulating signal processing and communications applications. First we will provide a general overview of Stateflow and how it can be used for logic design. We will then review fundamental Stateflow concepts through the use of a digital receiver examples, where Stateflow is used to model the synchronization logic of the receiver.
No Stateflow knowledge is assumed for those attending this webinar.

Πέμπτη 26/02/2009
Image and Video Processing with DSPs and FPGAs

Το σεμινάριο έχει διάρκεια 1 ώρα, και θα μεταδοθεί ζωντανά μέσω internet σε δύο ίδια Sessions.

- *Εγγραφή για τις 16.00*
- *Εγγραφή για τις 21.00*

Description:

In this webinar, we'll explore the concepts involved in migrating image and video processing algorithms to embedded processors such as DSPs or FPGAs. A demonstration using MATLAB and Simulink will cover:
• Converting an algorithm from floating-point to fixed-point math
• Adjusting an algorithm to accommodate small memory footprints
• Generating C code that targets DSPs
• Developing a Simulink model for an algorithm that targets FPGAs
• Verifying and testing results using hardware-in-the-loop
This webinar will highlight features in MATLAB and Simulink such as Embedded MATLAB code, fixed-point modeling, multirate modeling, C code generation, and hardware targets.

This webinar assumes some familiarity with MATLAB and Simulink. For users interested in learning about image and video processing capabilities in MATLAB prior to the webinar, demos are available on The MathWorks website here.

Σε περίπτωση που δεν μπορέσετε να παρακολουθήσετε τα webinars την ημέρα μετάδοσης τους, μπορείτε να τα βρείτε σε σύντομο χρονικό διάστημα

σε archived μορφή στην διεύθυνση: http://www.mathworks.com/company/events/archived_webinars.shtml

Με εκτίμηση,

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